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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ARCH D. ROBISON

Application No.: 10/037,774

Filed: January 3, 2002

For: **System and Method to Reduce the Size of Executable Code in a Processing System**

Art Group: 2122

Examiner: Not yet assigned

**RECEIVED**

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**Technology Center 2100**

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Assistant Commissioner for Patents  
Washington, D.C. 20231

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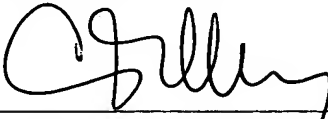
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: March 20, 2002

  
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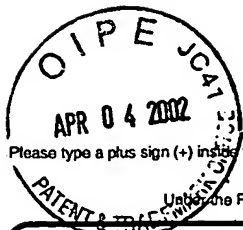
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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of

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### Complete if Known

Application Number	10/037,774
Filing Date	January 3, 2002
First Named Inventor	Arch D. Robison
Group Art Unit	2122
Examiner Name	Not yet assigned
Attorney Docket Number	42390P13132

### OTHER ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		WILLIAM WULF et al., The First Subphase, The Design of an Optimizing Compiler, Book, 1975, pages 110-114, American Elsevier Publishing Company, Inc.	
		WILLIAM WULF et al., Flo, The Design of an Optimizing Compiler, Book, 1975, pages 22-43, American Elsevier Publishing Company, Inc.	
		HENRI E. BAL et al., Language-and Machine-Independent Global Optimization on the Intermediate Code, Computer Language Vol.11 No. 2, 1986, pages 105-121, Pergamon Journals Ltd.	
		CHRISTOPHER W. FRASER, Analyzing and Compressing Assembly Code, Proceedings of the ACM SIGPLAN '84 Symposium on Compiler Construction, SIGPLAN Notices Vol.19, No.6, June 1984, pages 117-121.	
		KEITH D. COOPER et al., Enhanced Code Compression for Embedded RISC Processors, SIGPLAN '99 (PLDI) 5/99, pages 139-149, USA.	

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